

## INFORMAȚII PERSONALE

Gabriel Bonteanu

## POZIȚIA

Asistent Universitar

## EXPERIENȚA PROFESIONALĂ

02.2009-Prezent

**Asistent Universitar**

Universitatea Tehnica Gheorghe Asachi din Iasi

**Bazele Electronicii**

Dispozitive Electronice: Curs, Laborator si Seminar

Circuite electronice Fundamentale: Curs, Laborator si Proiect

Circuite Integrate Analogice: Laborator si Proiect

Circuite Integrate Digitale : Laborator

10.2007-02.2009

**Preparator Universitar**

Universitatea Tehnica Gheorghe Asachi din Iasi

**Bazele Electronicii**

Dispozitive Electronice: Laborator si Seminar

Circuite electronice Fundamentale: Laborator si Proiect

Circuite Integrate Analogice: Laborator si Proiect

Circuite Integrate Digitale : Laborator

08.2011-08.2019

**Inginer Proiectant de Circuite Integrate Analogice**

SC DAM Application SRL (Business Partner of Infineon Romania)

**Analog Circuit Design**

600nm:

Low Jitter Oscillator,  $\Sigma\Delta$  2<sup>nd</sup> Order Modulator

130nm:

CLKGEN (4MHz Low Jitter Oscillator + 16MHz PLL), GPADC ( $\Sigma\Delta$  1<sup>st</sup> Order)

16MHz Low Jitter Oscillator, Over Voltage Detector, SENT Receiver

OUTPUT DRIVER (with micro-break system), Micro-break Detector, Micro-break Data Storage

08.2004-03.2009

**Inginer Proiectant de Circuite Integrate Analogice**

LDIC-California / ATMEL ROMANIA / MOSYS

**Analog Circuit Design**

130nm INFINEON C11N:

CD/DVD/BD SOC : 2 GHz Frequency Synthesizer, Automatic Laser Power Control

SATA III/III PHY: TX/RX/PLL

65nm TSMC:

CD/DVD/BD SOC : 2 GHz Frequency Synthesizer, Automatic Laser Power Control

SATA III/III PHY: TX/RX

## EDUCAȚIE ȘI FORMARE

2010-2018

**Doctorat**

Replace with EQF  
(or other) level if  
relevant

Școala Doctorală a Universității Tehnice Gheorghe Asachi din Iași  
Conducător: Prof. Dr. Goraș Liviu  
TEZA: *Contribuții la dezvoltarea unor elemente de circuit controlate electric*

2000-2005

**Licența**

Replace with EQF  
(or other) level if  
relevant

Facultatea de Electronică și Telecomunicații  
Conducător: Conf. Dr. Cojan Neculai  
TEZA: *Transceiver UWB*

1996-2000

**Liceu**

Replace with EQF  
(or other) level if  
relevant

Colegiul Național Ștefan cel Mare și Sfânt Târgu Neamț  
Matematică-Fizică

## COMPETENTE PERSONALE

Limba(i) maternă(e)

Romanian

Îte limbi străine cunoscute

	INTELEGERE		VORBIRE		SCRIERE
	Ascultare	Citire	Participare la conversație	Discurs oral	
Engleza	B2	B2	B2	B2	B2
Franceza	A1	A1	A1	A1	A1

Competențe de comunicare

:Competențe de comunicare bune dobândite în urma experienței de predare dar și a celei de firmă

Permis de conducere

B

## INFORMATII SUPLIMENTARE

Brevete

**US20190052260A1**, "CIRCUIT AND METHOD FOR PROVIDING AN OUTPUT SIGNAL"  
<https://patents.google.com/patent/US20190052260A1/en?q=20190052260>

Publicații

**G. Bonteanu**, A. Cracan, "Enhanced wide range tunable CMOS transconductor for signal processing", Romanian Journal of Information Science and Technology, vol. 20, nr. 4, 2017  
**G. Bonteanu**, „A review of the transconductance control solutions”, Bulletin of the Polytechnic Institute of Iași, vol 63(67), nr. 4, 2017  
**G. Bonteanu**, „On the use of controlled gain current mirrors in the implementation of tuneable transconductors”, Bulletin of the Polytechnic Institute of Iași, vol 63(67), nr. 3, 2017  
**G. Bonteanu**, „A grounded capacitor to floating capacitor conversion circuit solution”, Bulletin of the Polytechnic Institute of Iași, vol 63(67), nr. 1, 2017

M. Mocanu, P. M. Puscasu, **G. Bonteanu** and L. Goras „*Describing function analysis of an LC oscillator structure*”, Bulletin of the Polytechnic Institute of Iași, Tomul LVIII, Fasc 3, 2012

**G. Bonteanu**, A. Cracan, R. G. Bozomitu; “ *A study on the Gm based current mode capacitance multipliers implementation*”, 2018 IEEE 24th International Symposium for Design and Technology in Electronic Packaging (SIITME)

**G. Bonteanu**, A. Cracan, R. G. Bozomitu; “ *A tunable transconductor with temperature and process immunity*”, 2018 IEEE 24th International Symposium for Design and Technology in Electronic Packaging (SIITME)

A. Cracan, **G. Bonteanu**, R. G. Bozomitu; “ *A weak-inversion CMOS analog multiplier/divider circuit*”, 2018 IEEE 24th International Symposium for Design and Technology in Electronic Packaging (SIITME)

**G. Bonteanu**, A. Cracan, “*Low Power and Low Area CMOS Capacitance Multiplier*”, 2018 International Semiconductor Conference (CAS), doi: 10.1109/SMICND.2018.8539846

A. Cracan, **G. Bonteanu**, “*Wide Dynamic Range Current Mirror*”, 2018 International Semiconductor Conference (CAS), doi: 10.1109/SMICND.2018.8539765

**G. Bonteanu**, “ *A Review of Capacitance Multiplication Techniques*”, ECAI 2018 - International Conference – 10th Edition, Electronics, Computers and Artificial Intelligence

**G. Bonteanu**, “ *A wide range fine tuning capacitance multiplier*”, 2017 IEEE 23rd International Symposium for Design and Technology in Electronic Packaging (SIITME), doi: 10.1109/SIITME.2017.8259873

**G. Bonteanu**, Arcadie Cracan, “*A tunable gyrator-capacitor active inductor*,” in The 23rd IEEE International Symposium for Design and Technology in Electronic Packaging (SIITME), Oct. 2017. doi: 10.1109/SIITME.2017.8259872

**G. Bonteanu**, “ *A current controlled CMOS current amplifier*”, 2017 5th International Symposium on Electrical and Electronics Engineering (ISEEE), doi: 10.1109/ISEEE.2017.8170668

**G. Bonteanu** and Arcadie Cracan, “*A high-gain programmable current mirror for large bias currents generation*,” 2017 5th International Symposium on Electrical and Electronics Engineering (ISEEE), Galati, Romania, 2017, pp. 1-4. doi: 10.1109/ISEEE.2017.8170675

**G. Bonteanu** and Arcadie Cracan, “*Wide range electrically controlled CMOS transconductor for adaptive signal processing*,” 2017 International Semiconductor Conference (CAS), Sinaia, 2017, pp. 301-304. doi: 10.1109/SMICND.2017.8101231

R. G. Bozomitu; N. Cojan; **G. Bonteanu**, „*A VLSI implementation of the 4<sup>th</sup> order elliptic fully differential IIR switched-capacitor low-pass filter in CMOS technology*”, 2013 IEEE 19th International Symposium for Design and Technology in Electronic Packaging (SIITME), doi: 10.1109/SIITME.2013.6743649

Damian Imbrea, Neculai Cojan, **G. Bonteanu**, “ *A 600 nA, 0.7 ppm/°C CMOS Voltage Reference Circuit without Resistors*”, IISCS 2011, 10.1109/IISCS.2011.5978739

Seminarii TI Precision Labs

Premii si distinctii Best poster award for senior scientist: SIITME 2018

Afilieri IEEE member

ANEXE

01.08.2019